



A-8753Y
PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Gerald J. BANKS

Appln. No.: 10/808,284

Group Art Unit: 2824

Filed: March 25, 2004

Examiner: V. Le

For: ELECTRICALLY ALTERABLE NON-VOLATILE MEMORY WITH
N-BITS PER CELL

Allowed: October 20, 2004

Confirmation No.: 5097

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Mail Stop: Issue Fee

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion
as to materiality or prior art effect, the documents listed
on the attached Form PTO-1449 are hereby cited.

Documents AJ-AP on the attached List were cited in a
01/27/2005 PBRITTON 00000002 501165 10808284
01 FC:1806 counterpart Japanese patent application (an English

translation of the Japanese Office Action is attached).

To facilitate the Examiner's consideration of Documents
AJ, AK, and AM, copies of English counterpart publications

US 5,940,704, WO 90/12400, and US 5,148,063 have been located are attached to the cited documents.

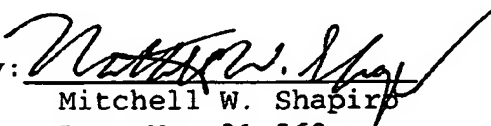
The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

STATEMENT UNDER 37 C.F.R. § 1.97(e)

The undersigned hereby states that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully requested,

By:


Mitchell W. Shapiro
Reg. No. 31,568

MWS:sjk

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December 8, 2004